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Abstract

5 A substrate of the present invention includes pads which
are provided on the surface of said substrate; and surface
layers which are kept to the ground potential and cover the
surface of said substrate except said pads and their
peripheral. Another substrate of the present invention
includes a part of circuit which is provided on the surface
of said substrate; and a surface layers which are kept to the
10 ground potential and cover the surface of said substrate
except said part of circuit and its peripheral.